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Notice of Allowability	Application No.	pplication No. Applicant(s)	
	10/614,663	KRENZKE ET AL.	
	Examiner	Art Unit	
	My-Trang N. Ton	2816	
The MAILING DATE of this communication apper All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85; NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	ears on the cover sheet water (OR REMAINS) CLOSED or other appropriate commiscipation is	in this application. If not included nunication will be mailed in due cours	se. THIS
1. This communication is responsive to <u>Tel. Int 05/03/04.</u>			
2. The allowed claim(s) is/are 1-31.			
3. $\boxtimes$ The drawings filed on <u>07 July 2003</u> are accepted by the Ex	xaminer.		
<ul> <li>4.  Acknowledgment is made of a claim for foreign priority unallocation.</li> <li>a)  All b)  Some* c)  None of the: <ol> <li>Certified copies of the priority documents have</li> <li>Certified copies of the priority documents have</li> <li>Copies of the certified copies of the priority documents have</li> <li>International Bureau (PCT Rule 17.2(a)).</li> </ol> </li> <li>* Certified copies not received:</li> </ul>	e been received. e been received in Applicat	on No	rom the
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		e a reply complying with the requirer	ments
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			E OF
6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") mus (a) ☐ including changes required by the Notice of Draftspers	son's Patent Drawing Revie	w ( PTO-948) attached	
<ol> <li>1) ☐ hereto or 2) ☐ to Paper No./Mail Date</li> <li>(b) ☐ including changes required by the attached Examiner'</li> <li>Paper No./Mail Date</li> </ol>		or in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on he header according to 37 C	the drawings in the front (not the back FR 1.121(d).	) of
7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT	SIT OF BIOLOGICAL MAT FOR THE DEPOSIT OF B	ERIAL must be submitted. Note t OLOGICAL MATERIAL.	the
Attachment(s) 1. ⊠ Notice of References Cited (PTO-892) 2. □ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. X Interview S	5. ☐ Notice of Informal Patent Application (PTO-152) 6. ☑ Interview Summary (PTO-413),	2)
<ul> <li>Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date <u>05/03/04</u></li> <li>☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material</li> </ul>	08), 7. ⊠ Examiner's	./Mail Date <u>05/03/04</u> . s Amendment/Comment s Statement of Reasons for Allowand —  MY-TRANG NUTON PRIMARY EXAMINEF	

Application/Control Number: 10/614,663

Art Unit: 2816

## **EXAMINER'S AMENDMENT**

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Att. Ackerman on 05/03/04.

## In the Specification

Under "Related patent Application" section, line 2, after "serial number", insert – 10/615,123 --; and after "filed on", insert – July 8, 2003 --.

## In the claims

In claim 1,

delete lines 2-5,

line 11, replace "signals" with - signal --;

line 12, before "resulting", insert – a --, and replace "signals" with – signal --;

after line 12, insert a paragraph: "combining said static currents and said proportional current reference into resulting reference current input;";

line 13, replace "signals" with – signal –; and before "into", insert – and said resulting reference current input --;

line 14, before "low", replace "the" with – a --;

Art Unit: 2816

In claims 17 and 20,

line 15, before "current", insert – resulting --; and replace "signals" with – signal and said resulting reference current input --;

last line, replace "high input voltage comparison" with – current comparator circuit

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In claims 2 and 3,
       lines 1-2, before "static", replace "a" with – the --;
       line 2, replace "level" with - levels --; and replace "current" with - currents --.
       In claims 4-5 and 8,
       line 1, before "high", replace – a – with "the";
       line 2, before "proportional", replace "a" with - the --.
       In claim 14,
       line 11, replace "reference current" with - proportional current reference --;
       line 12, delete "signal".
       line 14, before "reference" insert – resulting --;
       line 15, before "current" and before "reference", insert - resulting --;
       line 16, replace "current comparator" with - said current comparison circuit --;
       last line, replace "reference" with - high voltage reference input --; and before ".",
insert – representing the result of said current comparison circuit --.
       In claims 15-16,
       lines 1-2, before "static", replace "a" with – the --;
       line 2, replace "current" with - currents --.
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Application/Control Number: 10/614,663

Art Unit: 2816

line 1, before "high", replace - a - with "the";

line 2, before "proportional", replace "a" with - the --.

## Reasons for allowance

The following is an examiner's statement of reasons for allowance:

The present invention is drawn to a circuit capable of comparing higher voltage signals utilizing transforming means, combining means, feeding means, comparing means, generating means recited in claims 1-31. None of the prior art disclosed or suggested to show the particular structure and/or the particular operation recited in these claims namely: the combination of all method steps recited in claim 1; "means for transforming a static high voltage supply level into static currents", "means for combining ... into a resulting current input signal", "means for combining ... into a resulting reference current input", "means for feeding ... into a current comparison circuit", "means for comparing .... designated as said current comparison circuit" in combination with "means for transforming ... proportional current signal", "means for transforming ... proportional current signal", current comparison circuit" as recited in claim 14.

The prior art references submitted by the applicant on IDS forms received on 10/14/03 & 01/28/04 were reviewed and considered. These references appear to be the best references with respect to the claimed invention and closely matched most of the references found during the examiner's searches. However, as stated above, the combination of all the elements is not disclosed. Therefore, the claims are patentably distinct over all these prior art references of record.

Art Unit: 2816

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

MY-TRANG NUTON
PRIMARY EXAMINER

May 3, 2004